M.Tech. I Semester I Mid –Term Test Schedule - November – 2016

EXAM SECTION – NOTIFICATION DATED: 21.10.2016

M.Tech. I Semester (2016 Admitted Batch)   **Academic Year – 2016 - 17**   **TIMINGS: FN: 10:00 AM to12.00 Noon**
AN: 02:30 PM to 04:30 PM

DATE / DAY		Electrical Power Systems (EPS)	Advanced Manufacturing Systems (AMS)	Digital Electronics & Communication Systems (DECS)	Computer Science & Engineering (CSE)
03.11.2016 (Thursday)	FN	Modern Control Theory 16EPS101	Automation in Manufacturing 16AMS101	Structural Digital System Design 16DECS101	Advanced Operating Systems 16CSE101
	AN	Power System Dynamics and Stability 16EPS102	Advanced Manufacturing Processes 16AMS102	Advanced Computer Networks 16DECS102	Advanced Data Structures and Algorithms 16CSE102
04.11.2016 (Friday)	FN	Modern Power System Analysis 16EPS103	Material Technology 16AMS103	Digital Communication Techniques 16DECS103	Computer System Design 16CSE103
	AN	EHVAC Transmission 16EPS104	Product Design & Development 16AMS104	Advanced Digital Signal Procession 16DECS104	Advanced Computer Networks 16CSE104
05.11.2016 (Saturday)	FN	Analysis of Power Electronic Converters 16EPS105	Theory of Metal Cutting & Tool Design 16AMS105	Adaptive Signal Processing 16DECS105	Object Oriented Analysis & Design Patterns 16CSE105
	AN	Artificial Intelligence Techniques 16EPS401	Simulation & Modeling of Manufacturing Systems 16AMS404	Transform Techniques 16DECS401	Software Engineering 16CSE401

Note: All HODs are requested to circulate among the faculty in the department.

CONTROLLER OF EXAMINATIONS
Copy to:

1. All HODs :   EEE   ME   ECE   CSE
EC To:

2. SAO(G)   SAO (A&P)   S.P.O   AO(Systems)   SWO   Dean(R&D)   Dean(PGP)   TC   Librarian   SO   TI   Dy.Warden
3. Copy to HOD's to be read in M.Tech II Semester Class Rooms   4. Department & Academic Section Notice Boards

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